

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3106774	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/18 16:19
L2	30691	1 and amplif\$5 with (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/18 16:20
L3	3447	2 and (switch\$5 or transistor os isolat\$5 or transfer\$5) with between with amplif\$5 with (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/18 16:23
L4	866	3 and control\$5 with (switch\$5 or transistor or isolat\$5 or transfer\$5) with between with amplif\$5 with (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/18 17:08
L5	30	3 and control\$5 with (switch\$5 or transistor or isolat\$5 or transfer\$5) with between with amplif\$5 with (bit or bitline) with delay\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/18 16:24
L6	119	4 and 365/205.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/18 16:34
L7	37	4 and transfer\$5 near3 control adj2 circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/05/18 17:09